

## Shri Vaishnav Vidyapeeth Vishwavidyalaya Shri Vaishnav Institute of Technology and Science Choice Based Credit System (CBCS) Scheme in the light of NEP-2020 B.Tech. in Electrical and Electronics Engineering

SEMESTER-V (2023-2027)

	T		TI KELD I		(202	-5-20.	~ / )					
S. No.	COURSE CODE	COURSE NAME		TEACHING SCHEME/WEEK			EXAMINATION SCHEME					KS
						CREDITS	THEORY			PRACTICAL		AR
			L	Т	P		End Sem University Exam (60%)	Two Term Exam (20%)	Teachers Assess- ment* (20%)	End Sem University Exam (60%)	Teachers Assess- ment* (40%)	TOTAL M
1	BTEE503	Control System Engineering	3	1	2	5	60	20	20	. 30	20	150
2	BTME510	Designing Thinking and Innovation	2	0	0	2	60	20	20	0	0	100
. 3	BTEE501N	Electrical Machines II	3	1	2	5	60	20	20	30	20	150
4	-	Elective I	3	0	0	3	60	20	20	0	0	100
5	-	Generic Elective II	4	0	0	4	60	20	20	0	0	100
6	BTEEE501	Minor Project	()	0	4	2	0	0	0	30	20	50
. 7	BTEC507	Programming in Python	0	0	2	1	0	0	0	30	20	50
_	TOTAL				10	22	300	100	100	120	80	700

Legends: L - Lecture; T - Tutorial/Teacher Guided Student Activity; P - Practical;

<sup>\*</sup>Teacher Assessment shall be based on following components: Quiz/Assignment/Project/Participation in Class, given that no component shall exceed more than 10 marks.

S.	No.	Course Code	Elective I
	1	BTEE513	Antenna Theory
	2	BTEE514	Introduction of IOT in Electrical Engineering
	3	BTEC502	Cellular and Mobile Communication

Board of Studies Shri Vaishnay Vidyapeeth Vishwavidyalaya, Indore

Chairperson Faculty of Studies Shri Vaishuav Vidyapeeth Vishwavidyalaya, Indore

Controller of Examinations Shri Vaishnay Vidyapeeth Vishwavidyalaya, Indore

Registrar Shri Vaishnay Vidyapeeth Vishwavidyalaya, Indore

Vice Chancellor Shri Vaishnav Vidyapeeth

Vishwavidyalaya, Indore